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Diagram illustrating a 4-bit parallel adder using four full adders. The inputs are 1, 0, 3, and 1. The outputs show the carry propagation and the resulting sum bits: 1, 0, 1, 1. The final carry-out is 0.

[illegible][illegible]

Figure 1 illustrates the construction of a 14-bit word from four 4-bit words. The top row shows the 4-bit words: 0031, 3333, 1111, and 0000. The second row shows the 4-bit words: 33, 00, 00, and 00. The third row shows the 4-bit words: 33, 00, 00, and 00. The bottom row shows the 4-bit words: 33, 00, 00, and 00. The final 14-bit word is 0031333311110000.

[illegible]

Diagram illustrating a 23-bit bus system. The bus is divided into four segments, each 6 bits wide, with a final 5-bit segment. The segments are labeled 1 through 4. The bus is connected to four data lines (D0, D1, D2, D3) and a clock line (CLK). The data lines are labeled D0, D1, D2, and D3. The clock line is labeled CLK. The bus is divided into four segments, each 6 bits wide, with a final 5-bit segment. The segments are labeled 1 through 4. The bus is connected to four data lines (D0, D1, D2, D3) and a clock line (CLK). The data lines are labeled D0, D1, D2, and D3. The clock line is labeled CLK.

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